

# Concurrency Theory VS Concurrent Languages

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Bertinoro, OPCT 2014



**Bisimulation  
inside**

# Concurrency Theory VS Concurrent Languages

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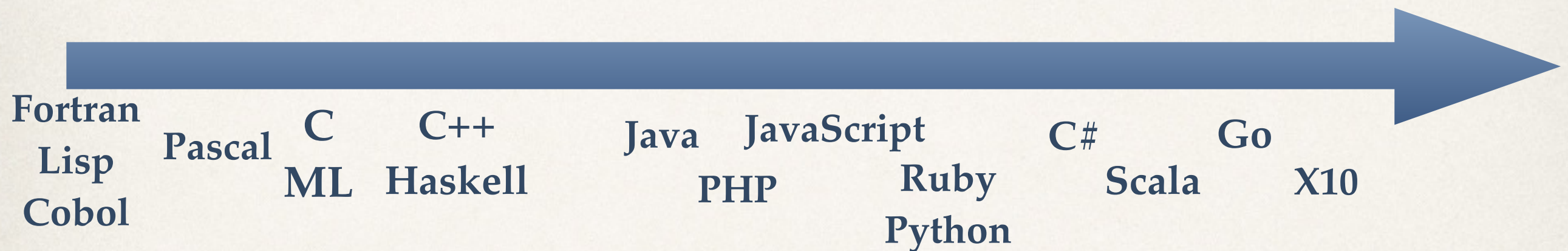
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# The Quest for good Abstractions

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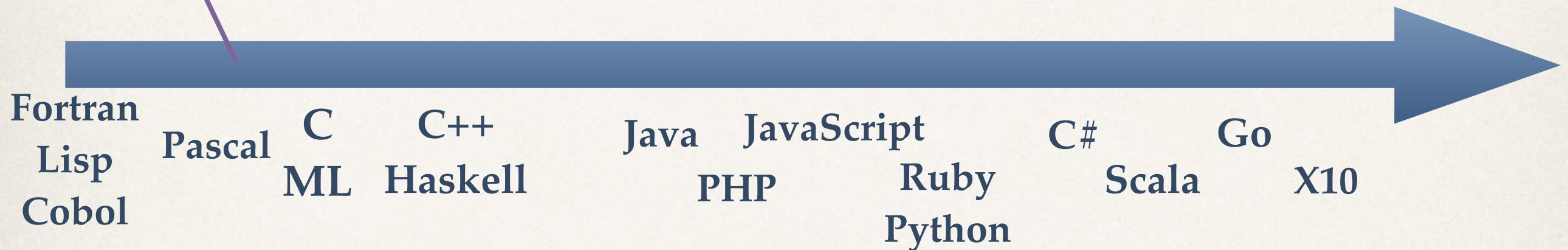


- ❖ When a language has been **invented** VS when became **popular**?
- ❖ **Why** has been invented VS why became popular?

# The Quest for good Abstractions

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Add structure  
to the code



- ❖ When a language has been **invented** VS when became **popular**?
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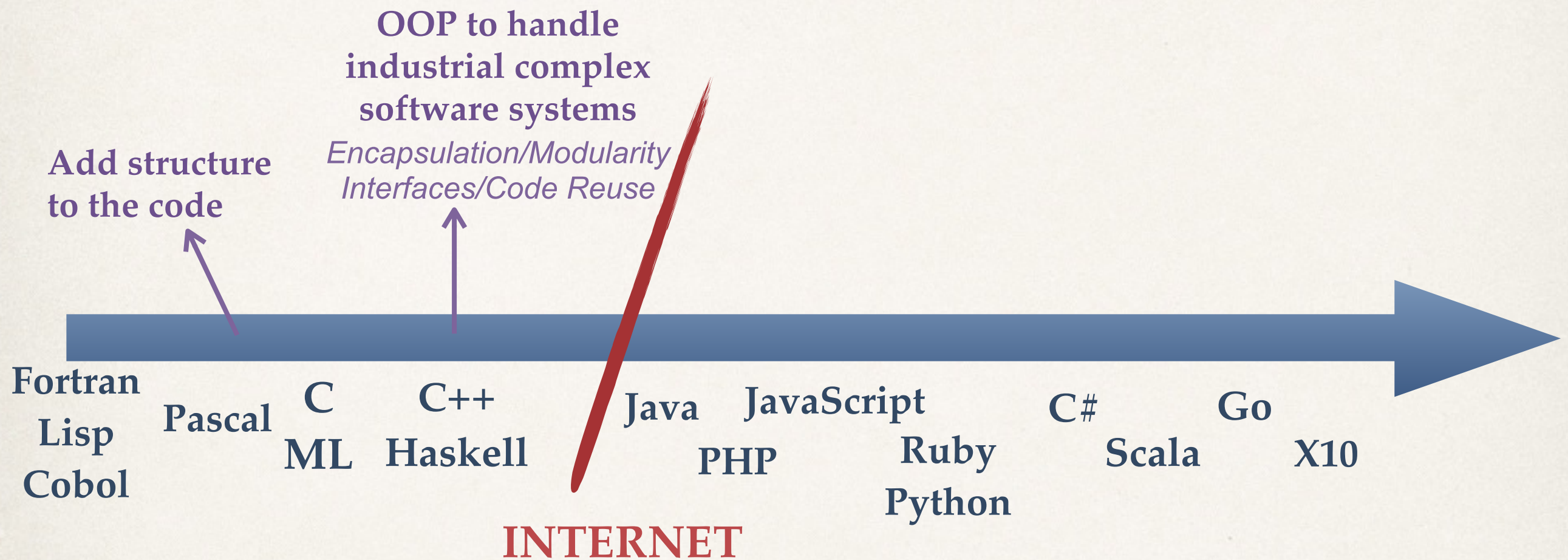
# The Quest for good Abstractions

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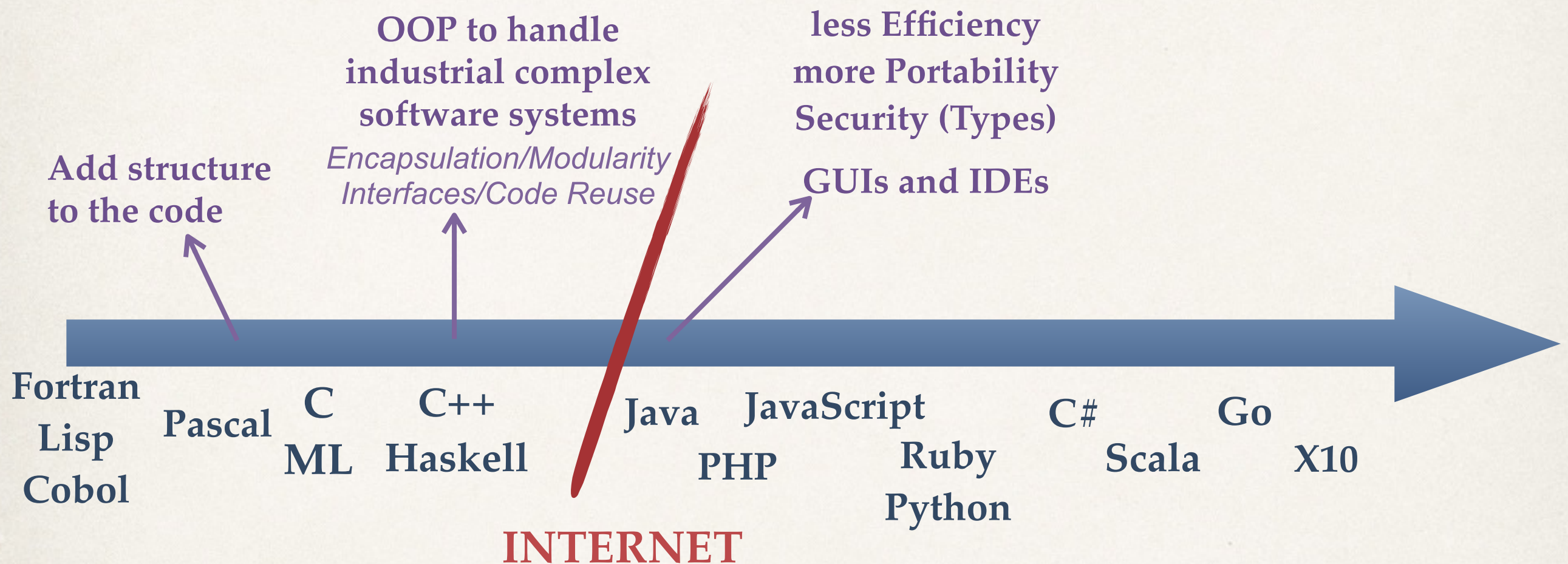
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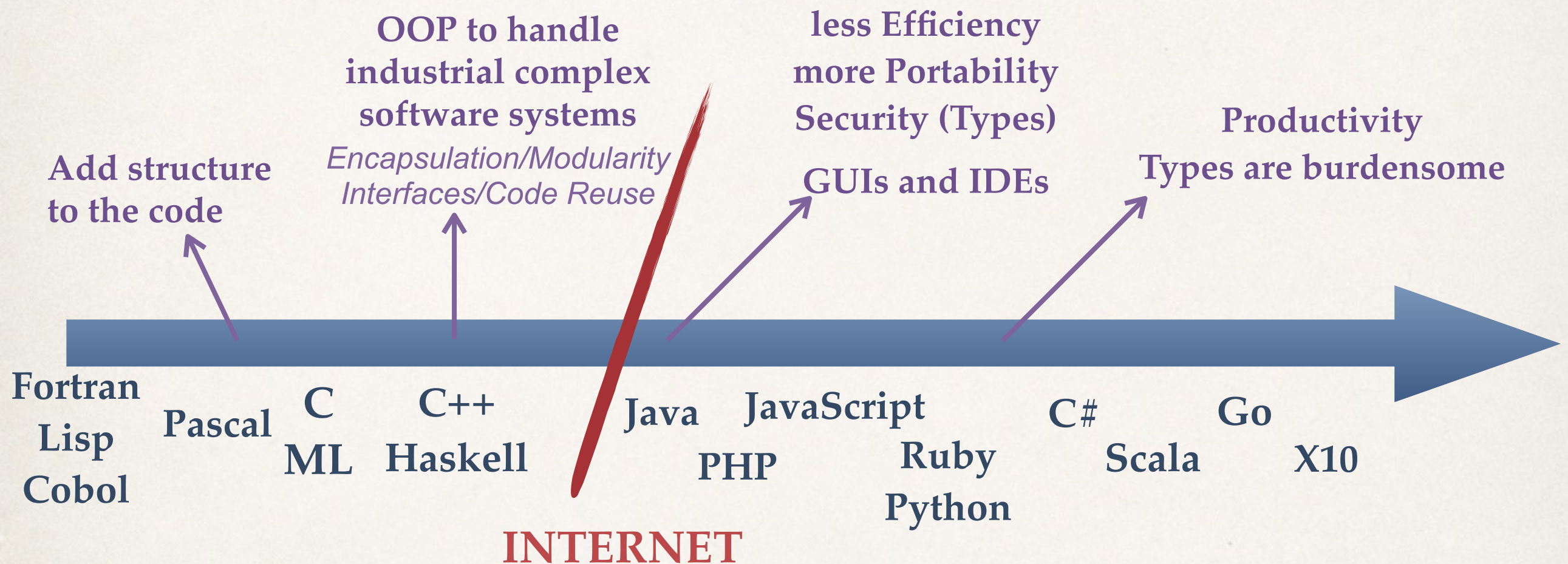


# The Quest for good Abstractions



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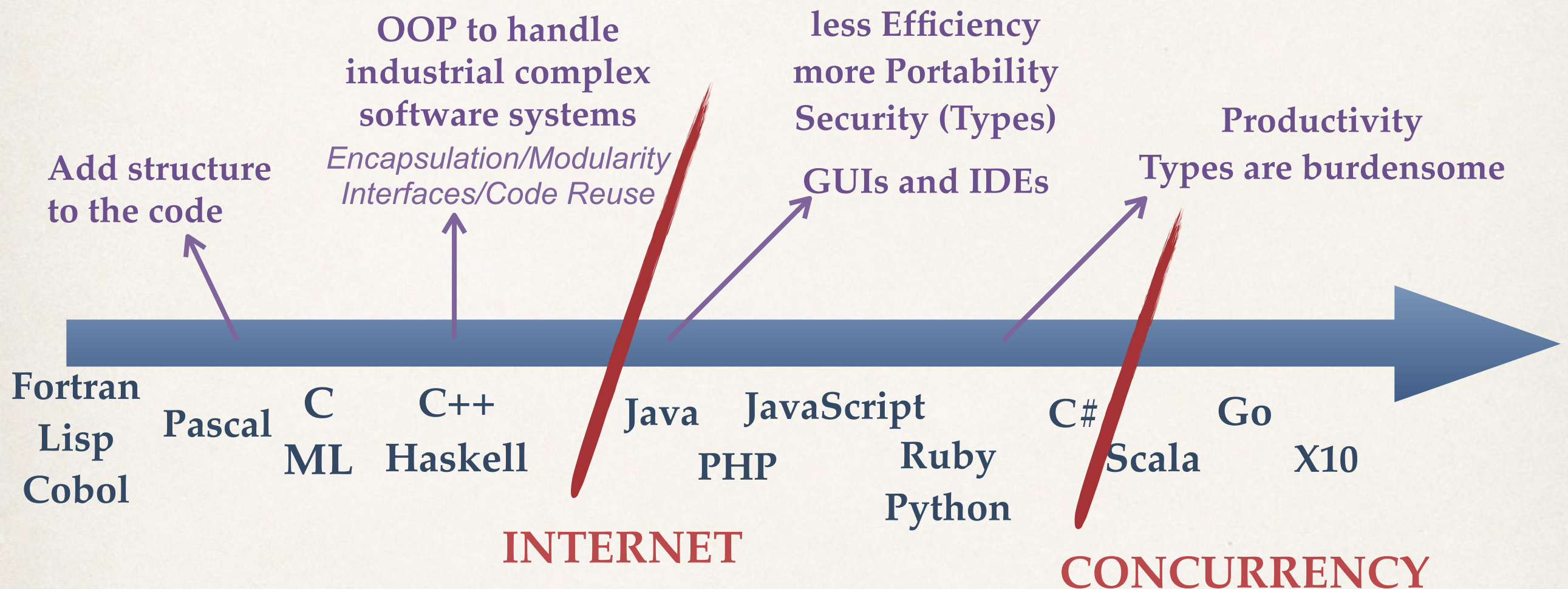
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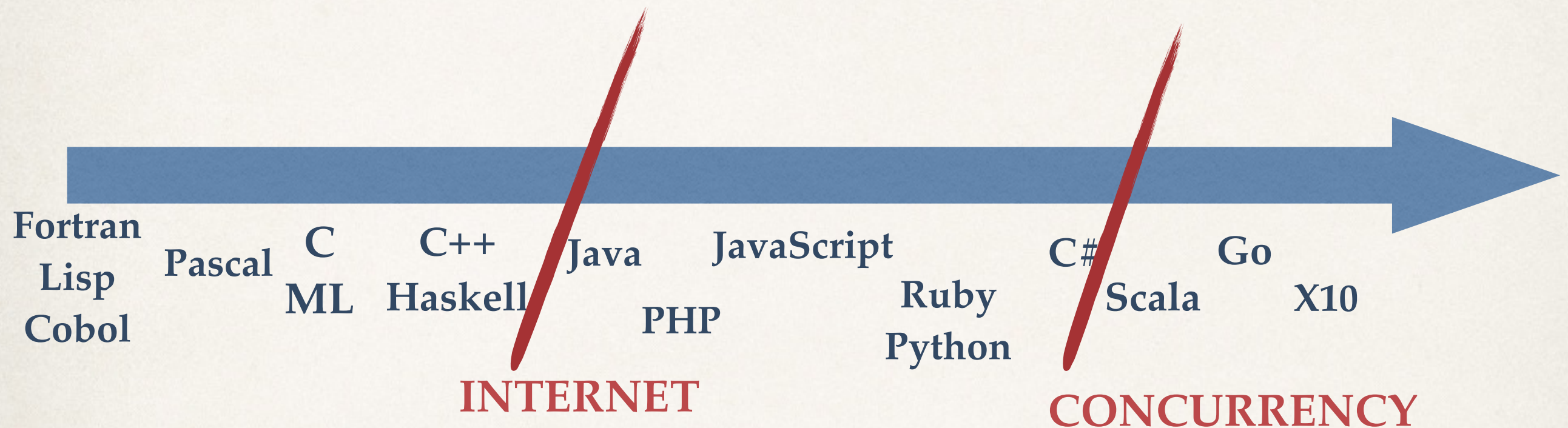
# The Quest for good Abstractions



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# The Quest for good Abstractions

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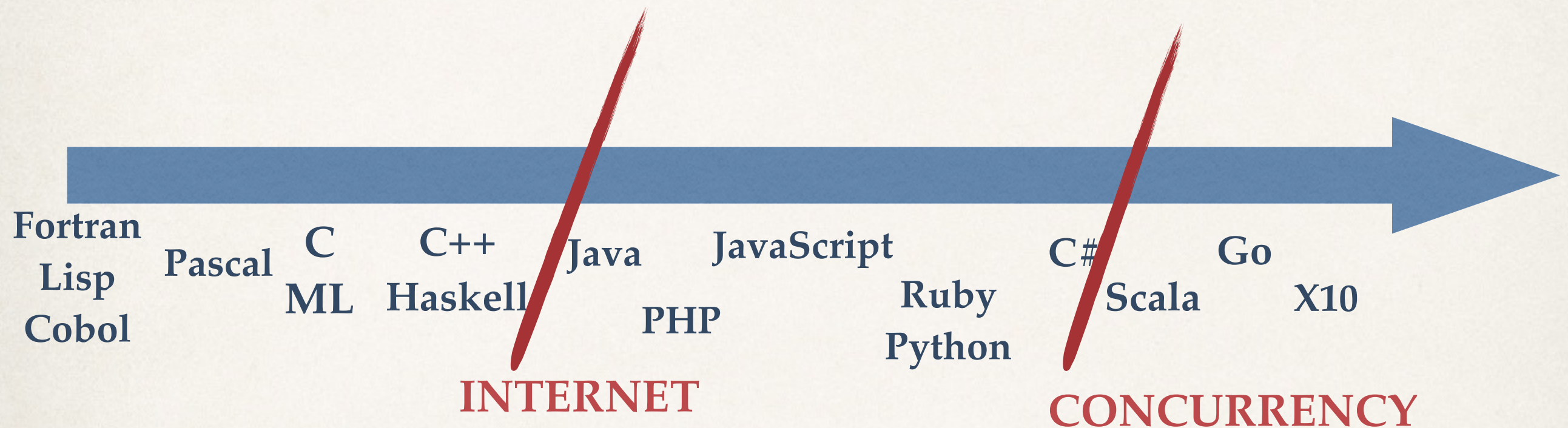


❖ Changes need a **catalyser**



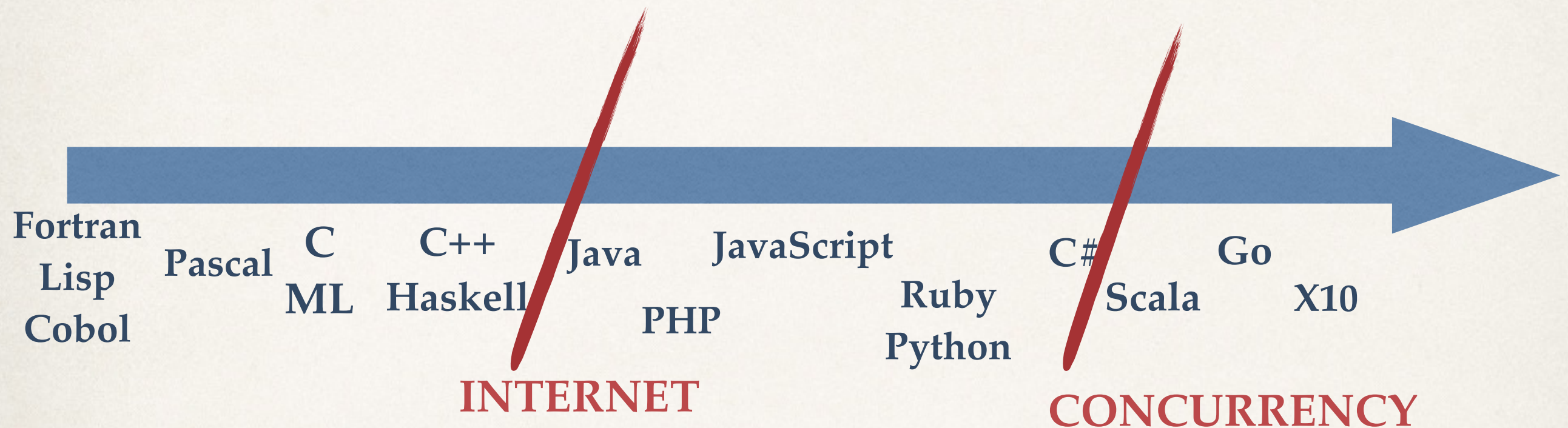
# The Quest for good Abstractions

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- ❖ Changes need a **catalyser**
  - ❖ **new hardware can only be parallel**
  - ❖ new software must be concurrent

# The Quest for good Abstractions



- ❖ Changes need a **catalyser**

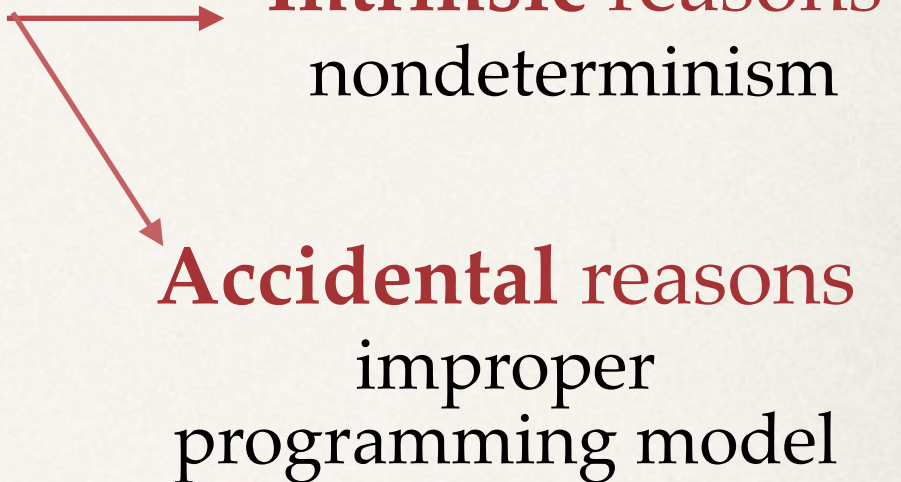
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**Popular  
Parallel Programming  
Grand Challenge**



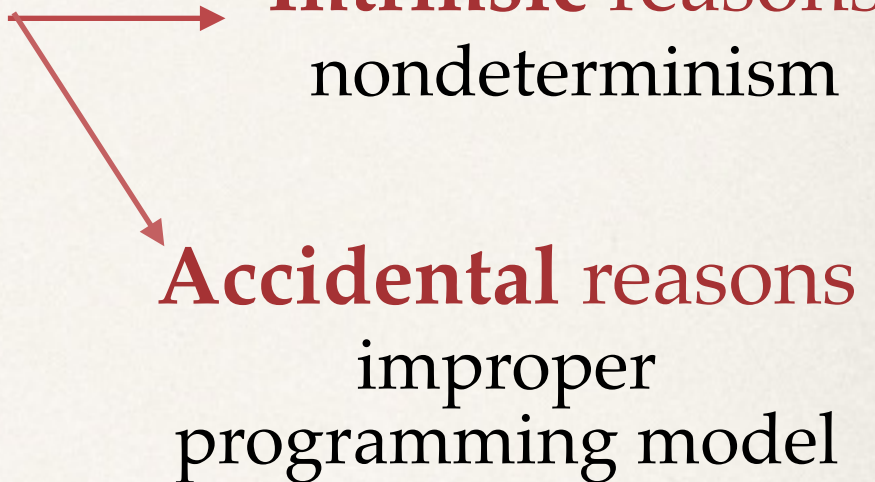
# How hard is Concurrent Programming?

---

- ❖ (correct) concurrent programming is **difficult**
  - ❖ **Adding** concurrency to sequential code is even harder
- 
- The diagram consists of two red arrows originating from the word "difficult" in the first list item. One arrow points horizontally to the right towards the text "Intrinsic reasons" and "nondeterminism". The other arrow points diagonally down and to the right towards the text "Accidental reasons", "improper", and "programming model".
- Intrinsic reasons**  
nondeterminism
- Accidental reasons**  
improper  
programming model

# How hard is Concurrent Programming?

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- A diagram with two red arrows originating from the word "difficult" in the first bullet point. One arrow points to the text "Intrinsic reasons" followed by "nondeterminism" on the next line. The other arrow points to the text "Accidental reasons" followed by "improper programming model" on the next two lines.
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*Think concurrently*  
**(Concurrent Algorithm)**

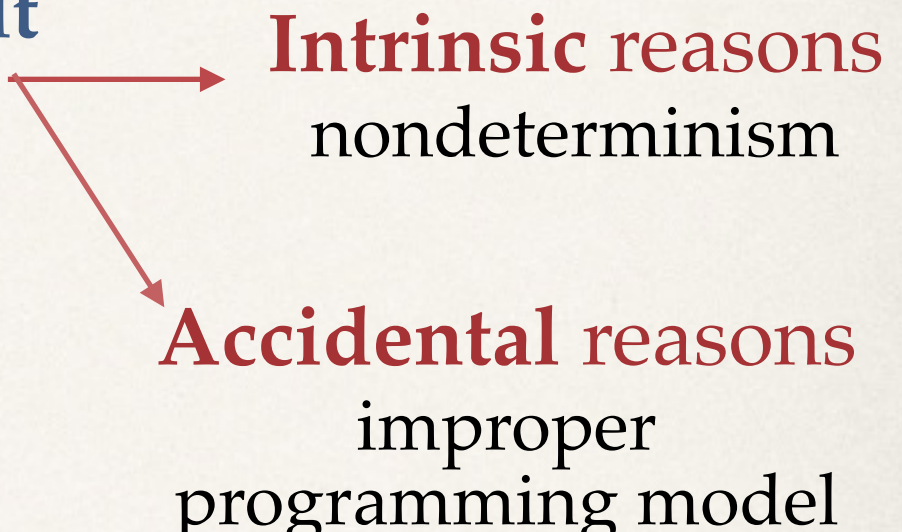


*Translate into*  
**a concurrent code**



# How hard is Concurrent Programming?

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- ❖ (correct) concurrent programming is **difficult**
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*Think concurrently*  
(Concurrent Algorithm)

*Translate into*  
a concurrent code



A yellow double-headed arrow connects the "Think concurrently" and "Translate into" text blocks. A red line descends from the center of this arrow and forms a red oval containing the text "DESIGN of concurrent language".

**DESIGN of  
concurrent language**



# How hard is Concurrent Programming?

- ❖ (correct) concurrent programming is **difficult**
  - ❖ **Adding** concurrency to sequential code is even harder
- Intrinsic reasons**  
nondeterminism
- Accidental reasons**  
improper programming model

*Think concurrently*  
(Concurrent Algorithm)

*Translate into*  
a concurrent code

**DESIGN of  
concurrent language**

**High-level  
Concurrency  
Abstraction**



# The Quest for good Abstractions

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Easy to think  
Easy to reason about



Expressiveness  
Performance

# The Quest for good Abstractions

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Expressiveness  
Performance

## ❖ OOP

- ❖ encapsulation
- ❖ memory management
- ❖ multiple inheritance



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C++ → Java → Scala

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C++ —> Java —> Scala

## ❖ Types

- ❖ documentation vs verbosity

C++ —> Java —> Ruby —> Scala



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C++ → Java → Scala

## ❖ Types

- ❖ documentation vs verbosity

C++ → Java → Ruby → Scala

## ❖ Functional Programming

- ❖ composing and passing behaviours
- ❖ sometimes imperative style is easier to reason about

C# → Scala C++11, Java8



# The Quest for good Abstractions

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Easy to think  
Easy to reason about



Expressiveness  
Performance

❖ OOP



which abstractions  
interoperate  
productively?

❖ Types

❖ Functional Programming



# The Quest for good Abstractions

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## Concurrency Abstractions?

Many Concurrency Models...

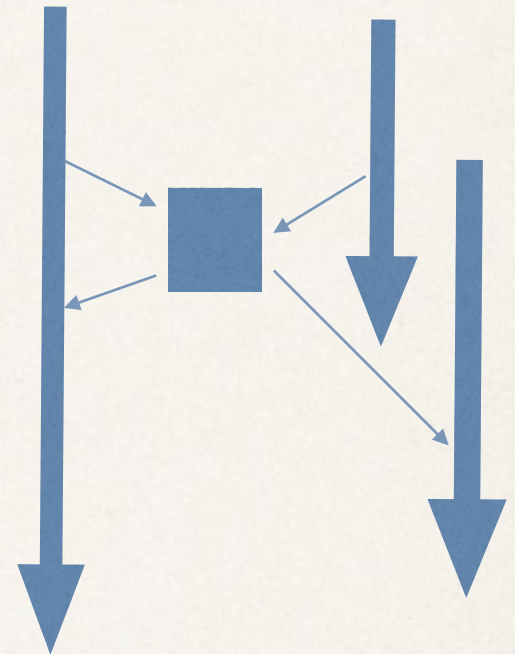
# The Quest for good Abstractions

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## Concurrency Abstractions?

Many Concurrency Models...

- ❖ Shared Memory Model and “Java Threads”





# The Quest for good Abstractions

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## Concurrency Abstractions?

Many Concurrency Models...

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Java

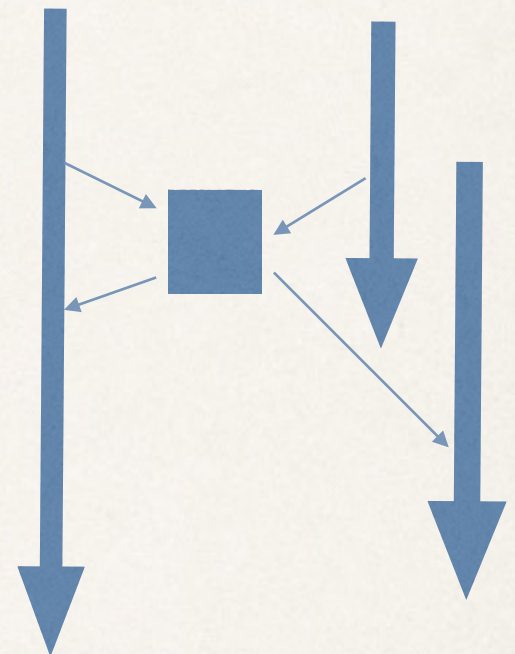
```
synchronized(lock)
lock.wait()
lock.notify()
```

STM

```
atomic {...}
when(cond) {...}
```

X10

```
async{}
finish{}
```



# The Quest for good Abstractions

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## Concurrency Abstractions?

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Java

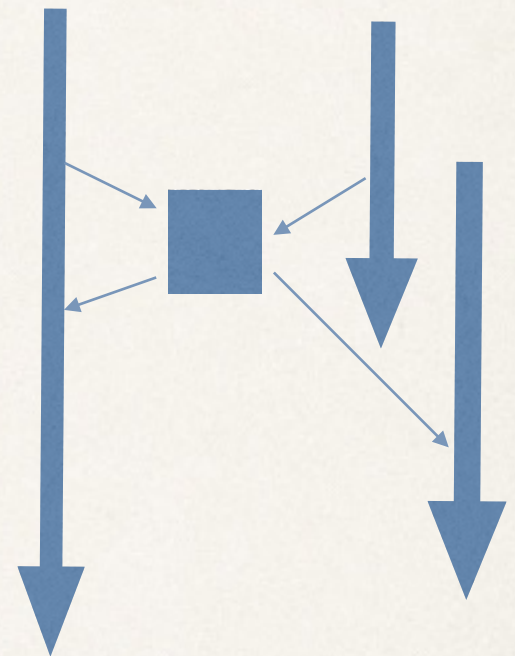
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- ❖ **logical threads** distinguished from **executors**  
(activities / tasks) (pool of thread workers)

*Scalability!*



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## Concurrency Abstractions?

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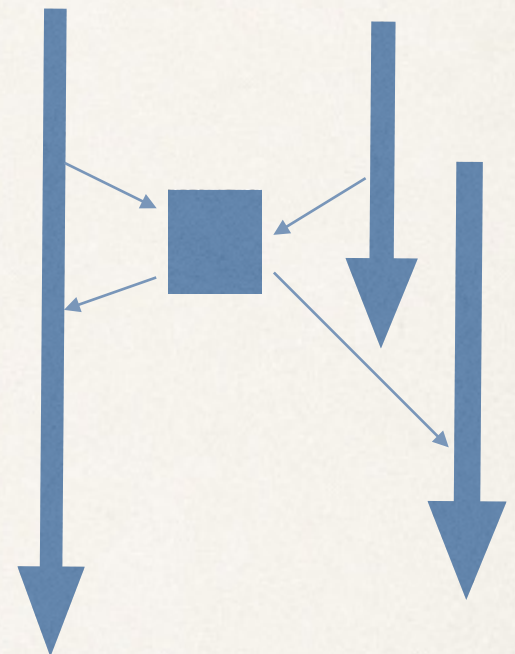
```
async{}
finish{}
```

```
new Thread().start()
JVM thread
```

Lightweight threads in the program  
Pool of Executors in the runtime

- ❖ **logical threads** distinguished from **executors**  
(activities / tasks) (pool of thread workers)

*Scalability!*





# Many Concurrency Models

---

## ❖ Shared Memory

- ❖ is very **natural** for “centralised algorithms” and components operating on shared data
- ❖ is **error-prone** when the sole purpose of SM is thread **communication**

## ❖ Message Passing Model

- ❖ It is the message that carries the state!
- ❖ **Channel based**: Google’s GO
- ❖ **Actor Model**: Erlang, Scala. It fits well both OOP and FP
- ❖ **Sessions**

## ❖ GPU Concurrency Model

- ❖ **Massive data parallelism**
- ❖ integration with high-level concurrent language (X10, Nova, Scala heterogeneous compiler)



# Many Concurrency Models

---

❖ Shared Memory

which abstractions  
interoperate  
productively?

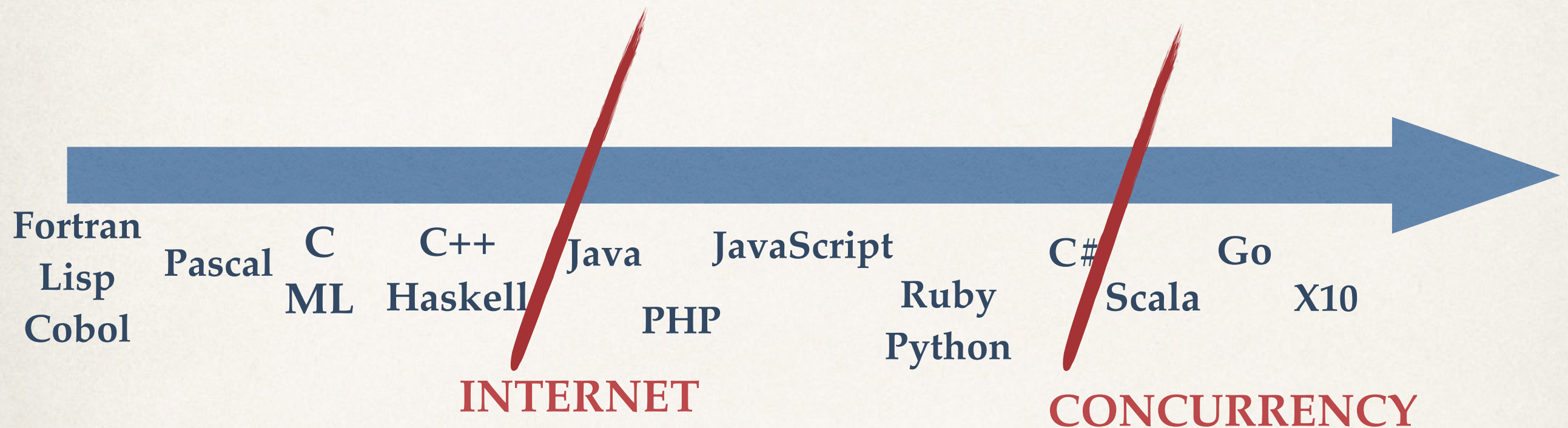


❖ Message Passing Model

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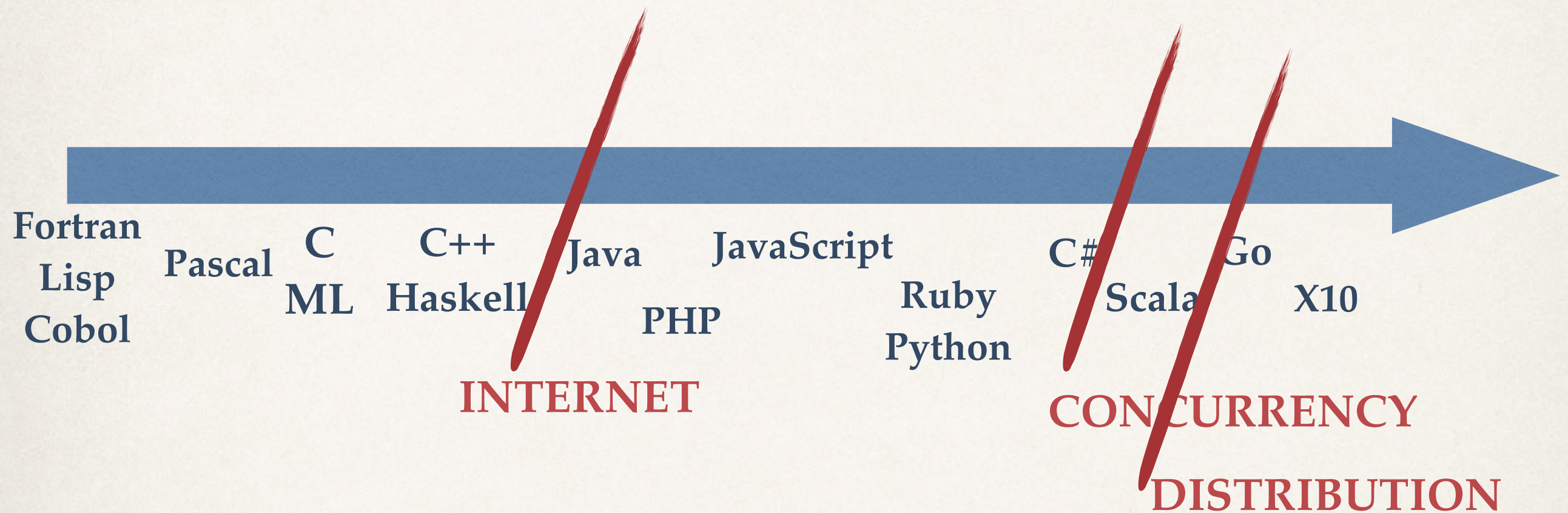


❖ New **catalyser**:



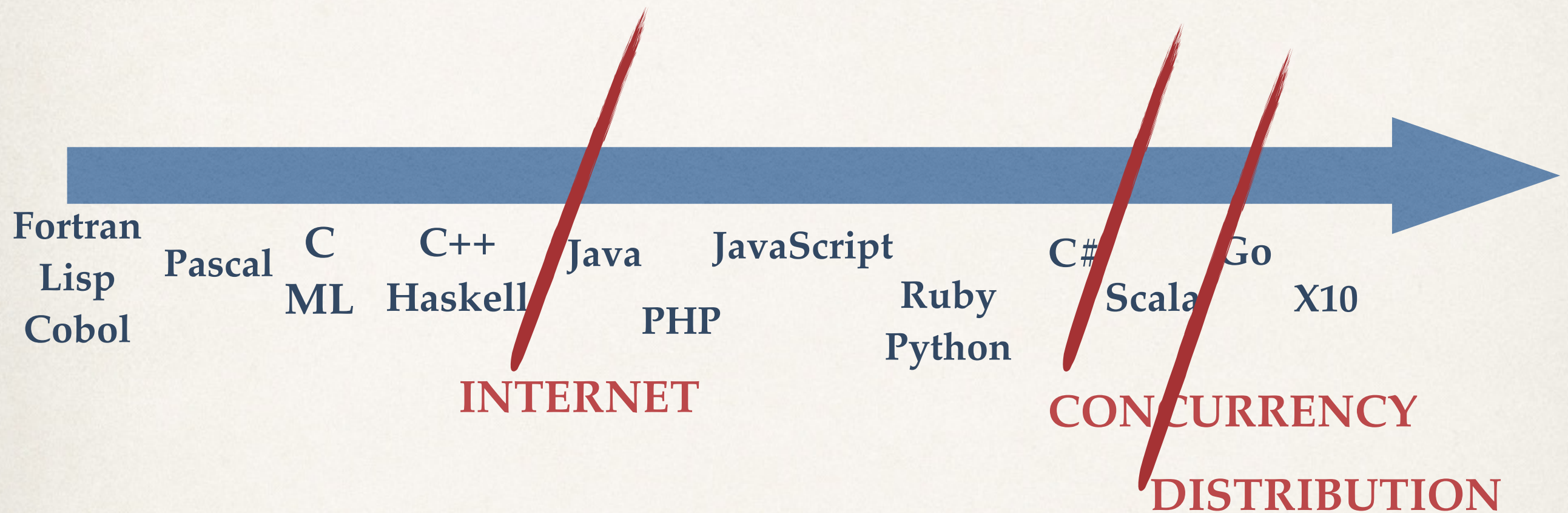
# The Quest for good Abstractions

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- ❖ New **catalyser**:
  - ❖ multicore —> concurrent programming
  - ❖ cloud computing —> distributed programming

# The Quest for good Abstractions



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**Reactive  
Programming**



# Reactive Programming

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- ❖ react to events

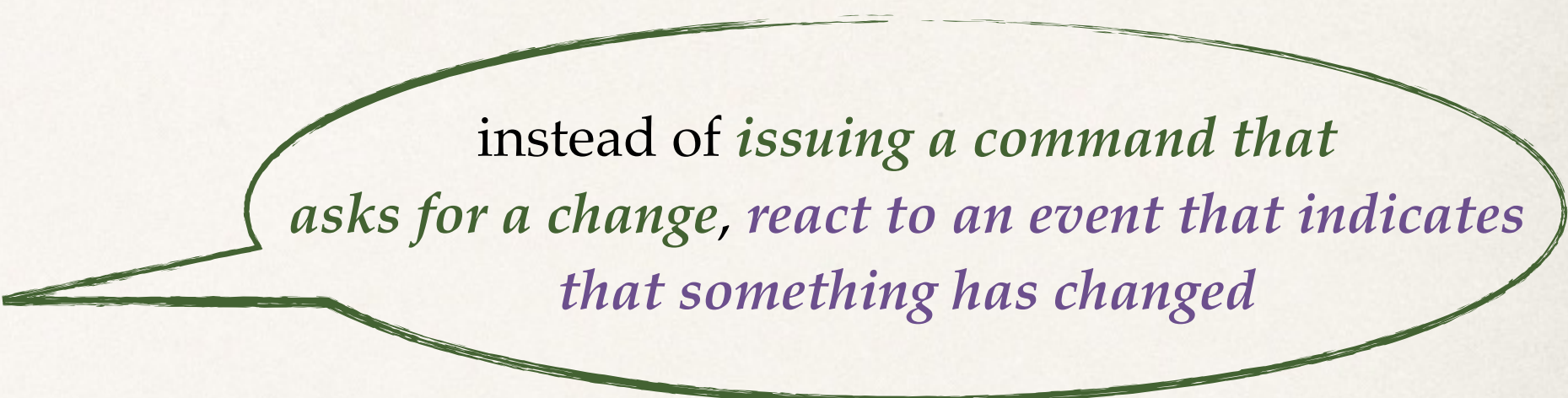
- ❖ react to load

- ❖ react to failures

# Reactive Programming

---

- ❖ react to events
  - ❖ **event - driven**
  - ❖ **asynchronous**



instead of *issuing a command that asks for a change*, *react to an event that indicates that something has changed*

- ❖ react to load

- ❖ **futures**

- ❖ **push data** to consumers when available rather than polling

- ❖ react to failures



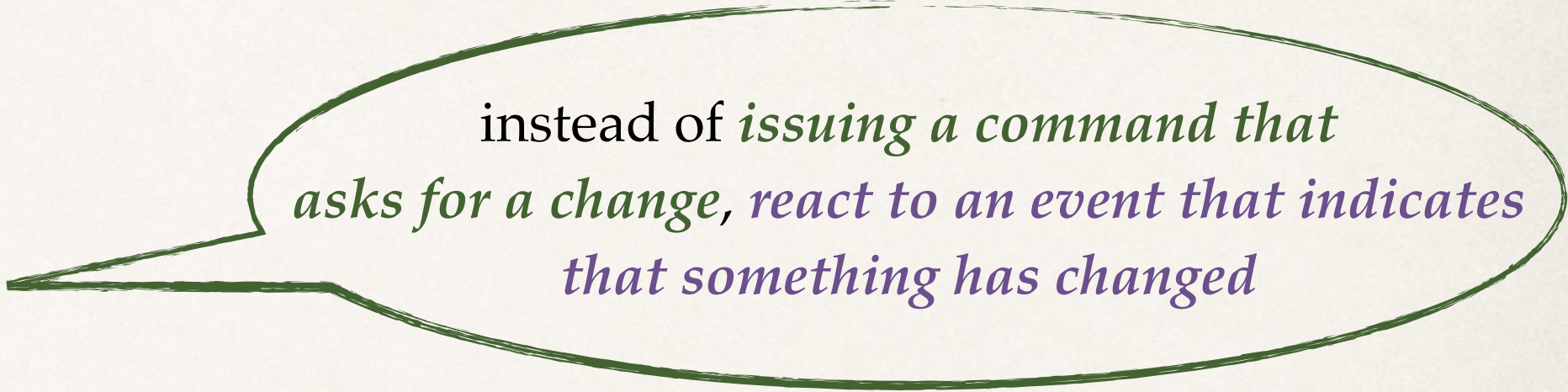
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- ❖ **scalability**

- ❖ up/down +/- CPU nodes

- ❖ in/out +/- server

- ❖ react to failures

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- ❖ **push data** to consumers when available rather than polling



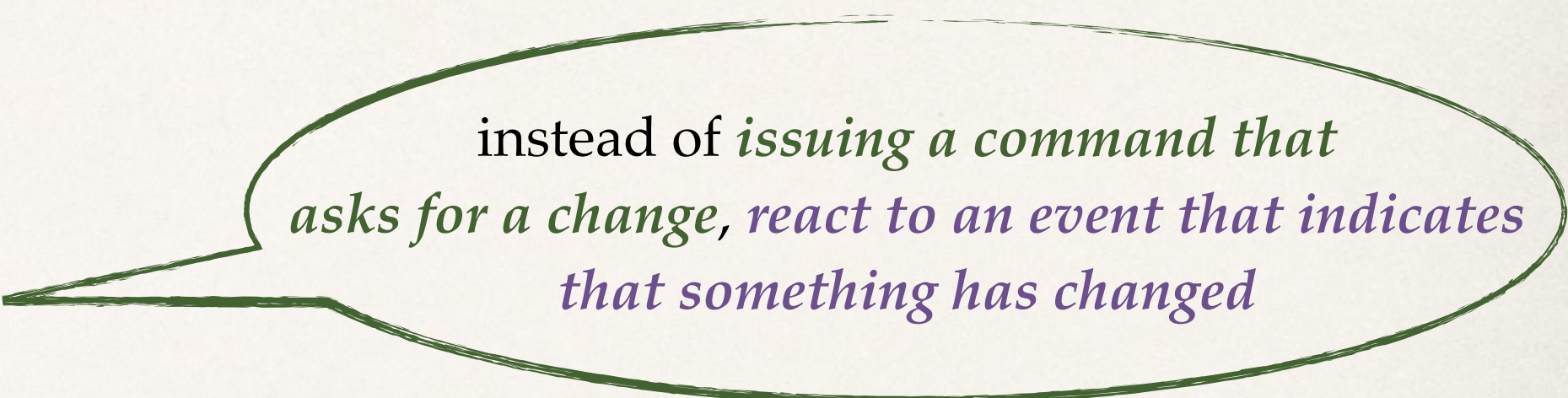
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- ❖ react to failures

- ❖ **resiliency**

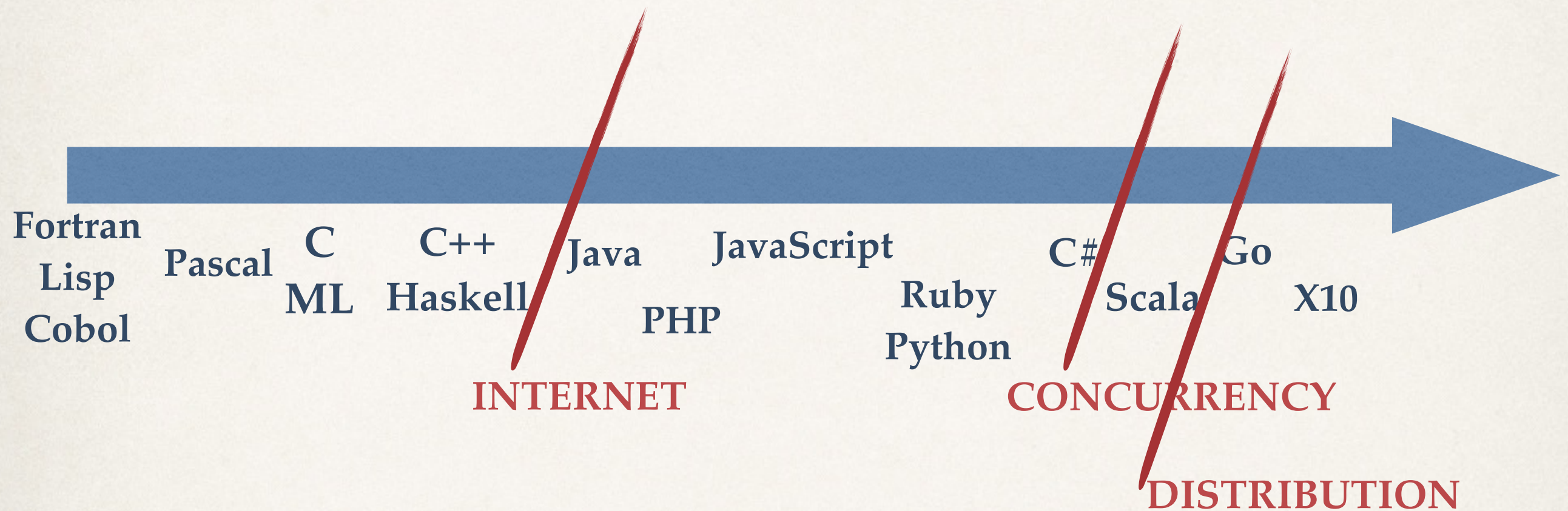
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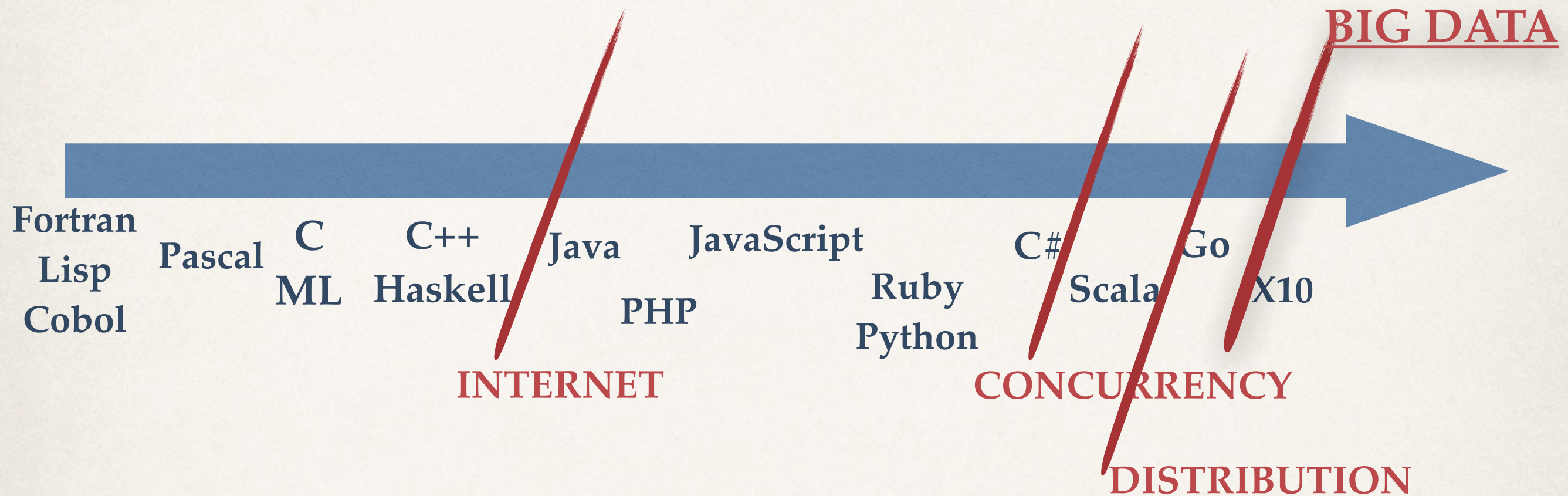
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❖ New **catalyser**:

- ❖ multicore —> concurrent programming
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# The Quest for good Abstractions



- ❖ New **catalyser**:

- ❖ multicore —> concurrent programming
- ❖ cloud computing —> distributed programming
- ❖ big data application —> High Performance Computing



# High Performance Computing

---

- ❖ scale-out on massively parallel hardware
  - ❖ high-performance computing on supercomputers
  - ❖ analytic computations on big data
- ❖ a single program
  - ❖ runs on a collection of places on a cluster of computers
  - ❖ can create global data-structures spanning multiple places
  - ❖ can spawn tasks at remote places, detecting termination of arbitrary trees of spawned tasks

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- ❖ **Big Data Application Framework**
  - ❖ **Map - Reduce Model**
  - ❖ **Bulk Synchronous Parallel Model**



# High Performance Computing

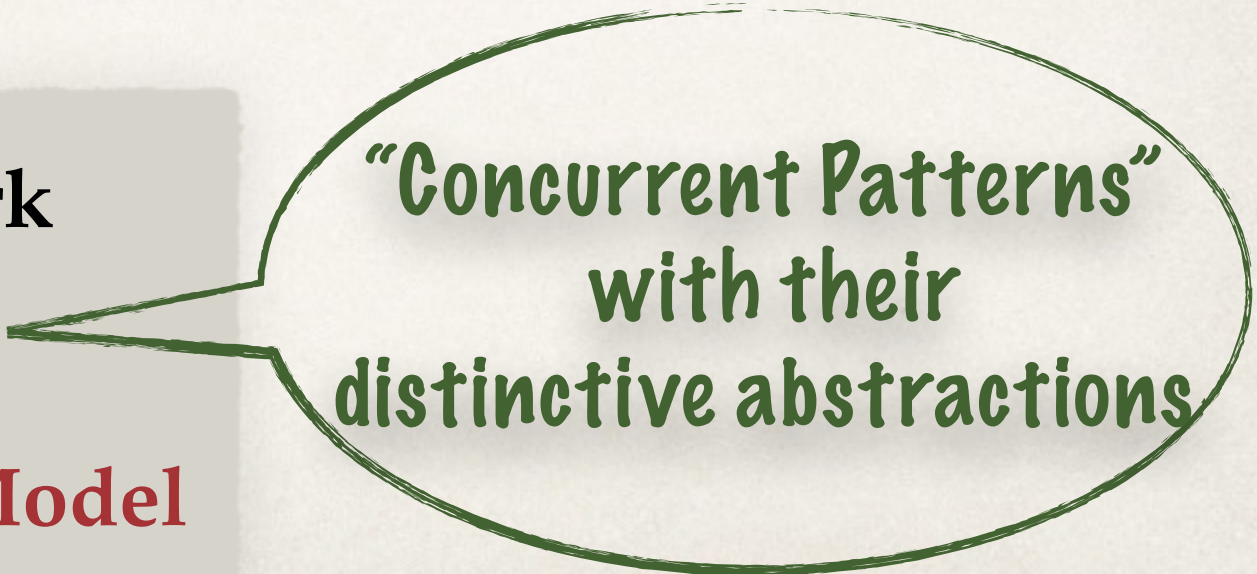
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- ❖ **Big Data Application Framework**

- ❖ **Map - Reduce Model**

- ❖ **Bulk Synchronous Parallel Model**



**“Concurrent Patterns”  
with their  
distinctive abstractions**

What about Theory ?

*The X10 experience*





# The X10 programming language

---

- ❖ open-source language for HPC programming
- ❖ key design features:
  - ❖ **scaling**: code running on 100 - 10.000 multicore nodes (up to 50millions core)
  - ❖ **productivity**: high level abstractions (Java-like, Scala-like) + typing (constrained dependent types as contracts).
  - ❖ **performance on heterogeneous hardware**: it compiles to Java, to C++, to CUDA. **Resilient extension**
  - ❖ **concurrent abstractions**: *place-centric, asynchronous computing*



# The X10 programming language

---

```
// double in parallel all the array elements
val a:Array[Int]= ...
    for(i in 0..(a.size-1))
        async { a(i)*=2 }
println ("The End")
```

Spawns an asynchronous  
lightweight activity  
running in parallel



# The X10 programming language

---

```
// double in parallel all the array elements  
val a:Array[Int]= ...  
finish for(i in 0..(a.size-1))  
    async { a(i)*=2 }  
println ("The End")
```

waits for the termination  
of all the spawned activities

Spawns an asynchronous  
lightweight activity  
running in parallel

# The X10 programming language

---

```
// double in parallel all the array elements
val a:Array[Int]= ...
var b=0
finish for(i in 0..(a.size-1))
    async { a(i)*=2
        atomic { b=b+a(i) }
    }
println ("The End")
```

STM    **when(cond) s**  
          clocks



# The X10 programming language

---

```
class HelloWorldWorld {  
    public static def main(args:Rail[String]) {  
        finish for (p in Place.places())  
            async at(p)  
                Console.OUT.println("Hello from place "+p)  
        Console.OUT.println("Hello from everywhere")  
    }  
}
```



# The X10 programming language

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}
```

```
%X10_NPLACES=4  
Hello from place 1  
Hello from place 2  
Hello from place 0  
Hello from place 3  
Hello from everywhere
```



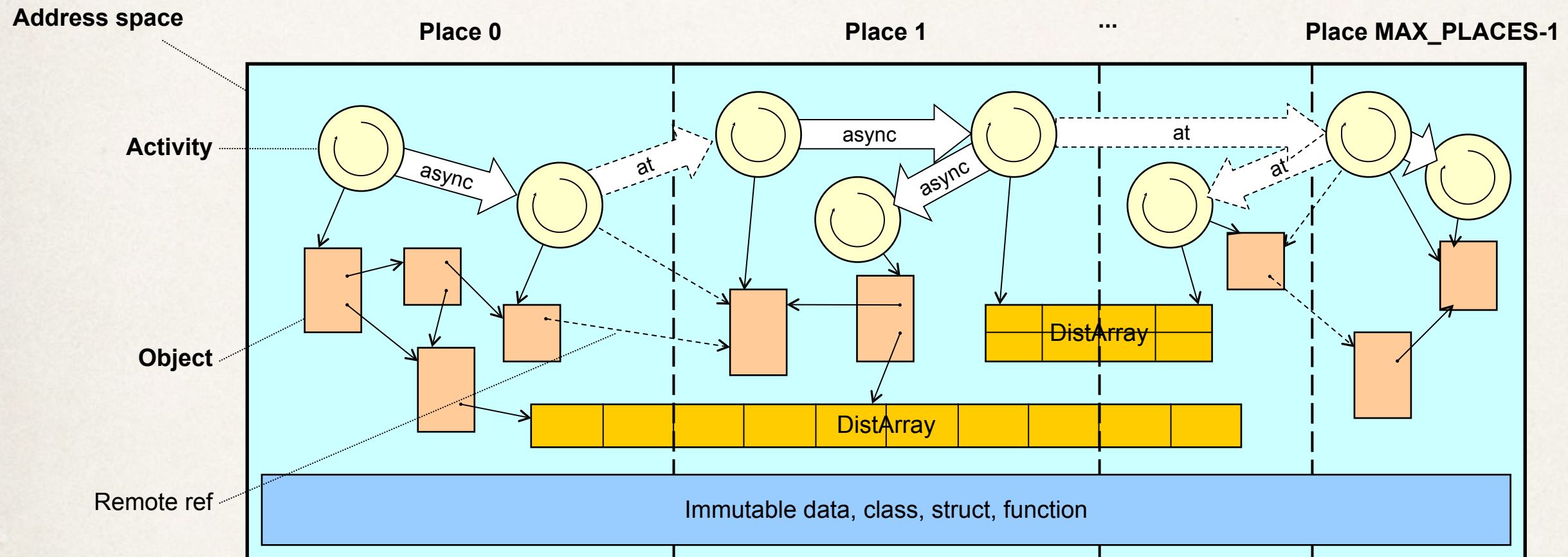
# The X10 programming language

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```
class HelloWorldWorld {  
    public static def main(args:Rail[String]) {  
        finish for (p in Place.places())  
            async at(p) @CUDA  
                Console.OUT.println("Hello from place "+p)  
        Console.OUT.println("Hello from everywhere")  
    }  
}
```

```
%X10_NPLACES=4  
Hello from place 1  
Hello from place 2  
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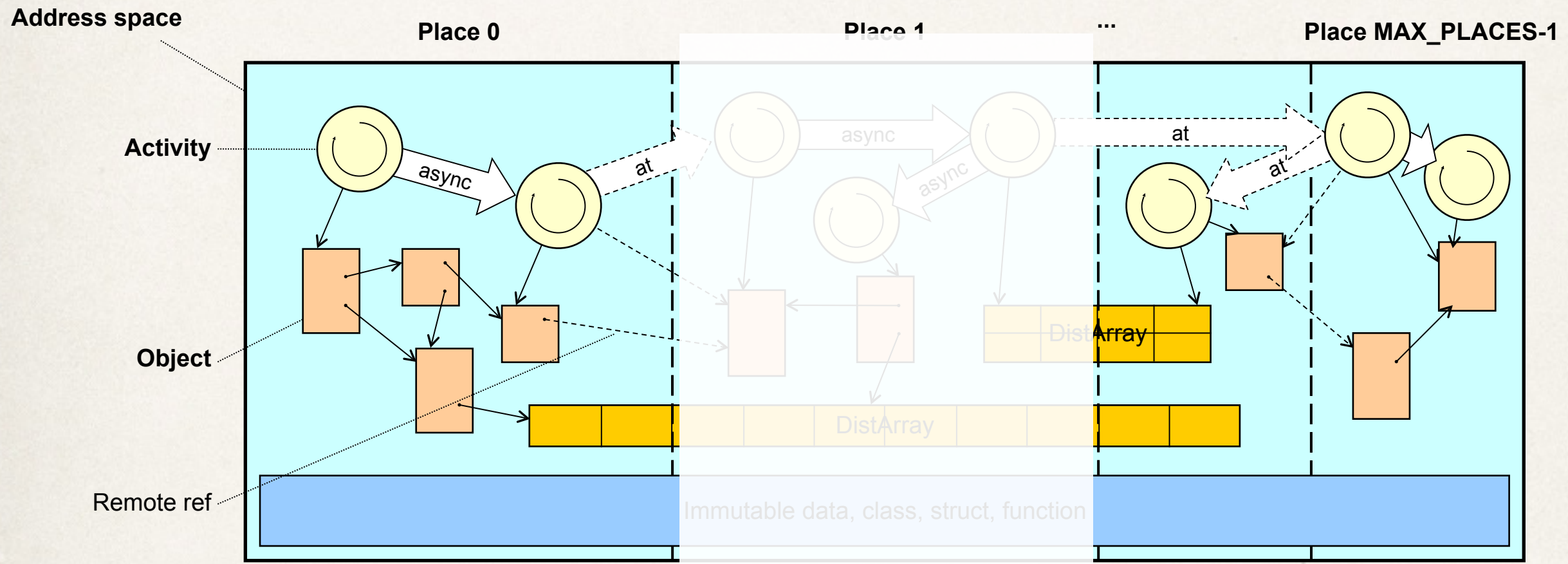
# Async Partitioned Global Address Space



- ❖ A global address space is divided into multiple *places* (=computing nodes)
  - ❖ Each place can contain *activities* and *objects*
- ❖ An object belongs to a specific place, but can be *remotely referenced*
- ❖ **DistArray** is a data structure whose elements are scattered over multiple places



# Resilient X10: if a node fails....



- ❖ it is relatively easy to **localize the impact of place death**
  - ❖ Objects in other places are still alive, but remote references become inaccessible
  - ❖ Execution continues using the remaining nodes
  - ❖ Happens Before Relation between remaining statements is preserved (HB Invariance) – no new race conditions, or sequentialization induced by failure.

# Resilient X10-like

Address space

Activity

Object

Remote ref

ACES-1

finish async at atomic clock  
local / global references  
place failures

can be mixed in any way

**SEMANTICS !!**

- ❖ it is relatively easy to **localize the impact of place death**
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# TX10

<i>Values</i>	$v ::=$	$o \mid o\$p \mid E \mid \text{DPE}$
<i>Expressions</i>	$e ::=$	$v \mid x \mid e.f \mid \{f:e, \dots, f:e\} \mid \text{globalref } e \mid \text{valof } e$
<i>Statements</i>	$s ::=$	$\text{skip}; \mid \text{throw } v \mid \text{val } x = e \text{ } s \mid e.f = e; \mid \{s \text{ } t\}$ $\text{at}(p)\text{val } x = e \text{ in } s \mid \text{async } s \mid \text{finish } s \mid \text{try } s \text{ catch } t$ $\overline{\text{at}(p)} s \mid \overline{\text{async}} s \mid \text{finish}_\mu s$
<i>Configurations</i>	$k ::=$	$\langle s, g \rangle \mid g$

*Global heap*  $g ::= \emptyset \mid g \cdot [p \mapsto h]$      *Local heap*  $h ::= \emptyset \mid h \cdot [o \mapsto (\tilde{f}_i : \tilde{v}_i)]$

# Semantics of (Resilient) X10

---

- ❖ Small-step transition system, mechanised in Coq
- ❖ **non in ChemicalAM style** (better fits the centralised view of the distributed program)

$$\langle s, g \rangle \longrightarrow_p \langle s', g' \rangle \mid g' \qquad \langle s, g \rangle \xrightarrow{E^\times}_p \langle s', g' \rangle \mid g' \qquad \langle s, g \rangle \xrightarrow{E^\otimes}_p \langle s', g' \rangle \mid g'$$



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**Async failures** arise in parallel threads  
and are caught by the inner `finish` waiting for their termination

**`finish {async throw E    async s2}`**



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**Async failures** arise in parallel threads  
and are caught by the inner `finish` waiting for their termination

**`finish {async throw E    async s2}`**

**Sync failures** lead to the failure of any sync continuation  
leaving async (remote) running code free to terminate

**`{async at (p) s1    throw E    s2}`**



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$$\langle s, g \rangle \longrightarrow_p \langle s', g' \rangle \mid g' \qquad \langle s, g \rangle \xrightarrow{E \times}_p \langle s', g' \rangle \mid g' \qquad \langle s, g \rangle \xrightarrow{E \otimes}_p \langle s', g' \rangle \mid g'$$

Proved  
in Coq

**Async failures** arise in parallel threads  
and are caught by the inner `finish` waiting for their termination

**`finish {async throw E    async s2}`**

Proved  
in Coq

**Sync failures** lead to the failure of any sync continuation  
leaving async (remote) running code free to terminate

**`{async at (p) s1    throw E    s2}`**

# Semantics of (Resilient) X10

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Proved  
in Coq

Absence of stuck states

(the proof can be run, yielding an interpreter for TX10)



# Semantics of Resilient X10

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**smoothly scales to node failure, with**

- ❖ global heap is a partial map: *dom(g)* **collects non failed places**
- ❖ executing a statement at failed place results in a DPE
- ❖ place shift at failed place results in a DPE
- ❖ remote exceptions flow back at the remaining finish **masked** as DPE

*contextual rules  
modified accordingly*

**(Place Failure)**

$$\frac{p \in \text{dom}(g)}{\langle s, g \rangle \longrightarrow_p \langle s, g \setminus \{(p, g(p))\} \rangle}$$

$$\frac{p \notin \text{dom}(g)}{\begin{array}{l} \langle \text{skip}, g \rangle \xrightarrow[p]{\text{DPE} \otimes} g \\ \langle \text{async } s, g \rangle \xrightarrow[p]{\text{DPE} \otimes} g \\ \langle \text{at}(p) s, g \rangle \xrightarrow[q]{\text{DPE} \otimes} g \end{array}}$$

# Semantics of Resilient X10

---

## ❖ Happens Before Invariance

- ❖ failure of place  $q$  does not alter the happens before relationship between statement instances at places other than  $q$

$\overline{\text{at}}(0) \{ \overline{\text{at}}(p) \text{ finish } \overline{\text{at}}(q) \overline{\text{async}} s_1 \quad s_2 \}$        *$s_2$  runs at 0 after  $s_1$*

$\overline{\text{at}}(0) \text{ finish } \{ \overline{\text{at}}(p) \{ \overline{\text{at}}(q) \overline{\text{async}} s_1 \} \quad s_2 \}$        *$s_2$  runs at 0 in parallel with  $s_1$*



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**$p$  fails while  $s_1$   
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**same behaviour!**

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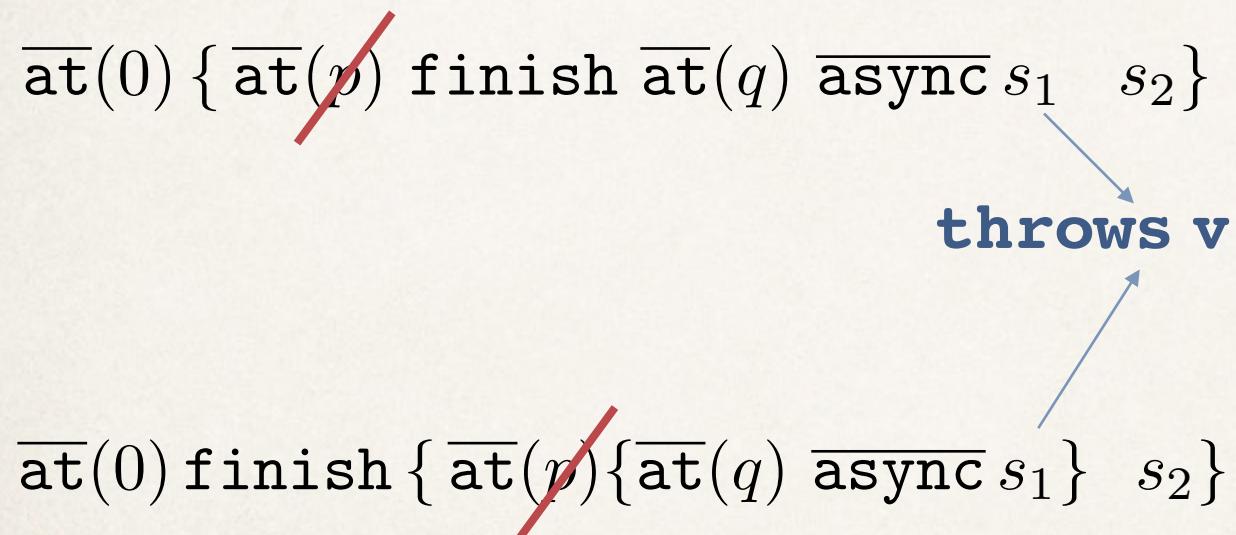
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$\text{DPE} \otimes$  *flows at place 0 discarding  $s_1$*



$v \times$  *flows at place 0 while  $s_2$  is running*

# Equational theory for (Resilient) X10

---

$\langle s, g \rangle \cong \langle t, g \rangle$  **equivalent configurations** when

- ❖ transition steps are weakly bi-simulated
- ❖ *under any modification of the shared heap by current activities*  
(object field update, object creation, place failure)



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$\langle s, g \rangle \mathcal{R} \langle t, g \rangle$  whenever

1.  $\vdash \text{isSync } s \text{ iff } \vdash \text{isSync } t$
2.  $\forall p, \forall \Phi$  environment move

if  $\langle s, \Phi(g) \rangle \xrightarrow{\lambda}_p \langle s', g' \rangle$  then  $\exists t'. \langle t, \Phi(g) \rangle \xRightarrow{\lambda}_p \langle t', g' \rangle$

with  $\langle s', g' \rangle \mathcal{R} \langle t', g' \rangle$  and viceversa



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models the update of  $g$ :

$\text{dom}(\Phi(g)) = \text{dom}(g)$  and  
 $\forall p \in \text{dom}(g) \text{ dom}(g(p)) \subseteq \text{dom}(\Phi(g)(p))$



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**Bisimulation whose Bisimilarity is a congruence**



# Equational theory for (Resilient) X10

---

$$\{\{s \ t\} \ u\} \cong \{s \ \{t \ u\}\}$$

$$\vdash \text{isAsync } s \quad \text{try } \{s \ t\} \text{ catch } u \cong \{\text{try } s \text{ catch } u \ \text{try } t \text{ catch } u\}$$

$$\text{at}(p)\{s \ t\} \not\cong_{\mathbf{R}} \{\text{at}(p)s \ \text{at}(p)t\}$$

$$\text{at}(p)\text{at}(q)s \not\cong_{\mathbf{R}} \text{at}(q)s$$

$$\text{async at}(p)s \not\cong_{\mathbf{R}} \text{at}(p) \text{async } s$$

$$\text{finish } \{s \ t\} \cong \text{finish } s \ \text{finish } t$$

$$\text{finish } \{s \ \text{async } t\} \cong \text{finish } \{s \ t\}$$

$$\text{finish at}(p)s \not\cong_{\mathbf{R}} \text{at}(p) \text{finish } s$$

if  $s$  throws a sync exc.  
and home is failed,  
then l.h.s. throws a  
masked DPE<sub>ex</sub> while  
r.h.s. re-throws  $vx$   
since synch exc are  
not masked by DPE



# Conclusions

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- ❖ **Concurrency is critical for Programming Languages**
  - ❖ heterogeneous concurrency models (Distribution)
- ❖ **What is the right level of abstraction?**
  - ❖ What are good abstractions? Expressive, flexible, easy to reason about, easy to implement in a scalable / resilient way
- ❖ **Formal method to experiment!**
  - ❖ test new primitive, *new mix* of primitives
  - ❖ tool to reason about programs

**(Par Left)**

$$\langle s, g \rangle \xrightarrow{\lambda}_p \langle s', g' \rangle \mid g'$$


---

$$\lambda = \epsilon, v \times \quad \langle \{s \ t\}, g \rangle \xrightarrow{\lambda}_p \langle \{s' \ t\}, g' \rangle \mid \langle t, g' \rangle$$

$$\lambda = v \otimes \quad \langle \{s \ t\}, g \rangle \xrightarrow{\lambda}_p \langle s', g' \rangle \mid g'$$

**(Par Right)**

$$\frac{\vdash \text{isAsync } t \quad \langle s, g \rangle \xrightarrow{\lambda}_p \langle s', g' \rangle \mid g'}{\langle \{t \ s\}, g \rangle \xrightarrow{\lambda}_p \langle \{t \ s'\}, g' \rangle \mid \langle t, g' \rangle}$$

**(Place Shift)**

$$(v', g') = \text{copy}(v, q, g)$$


---

$$\langle \text{at}(q) \text{val } x = v \text{ in } s, g \rangle \longrightarrow_p \langle \overline{\text{at}}(q) \{s[v' / x] \text{ skip}\}, g' \rangle$$

**(At)**

$$\frac{\langle s, g \rangle \xrightarrow{\lambda}_q \langle s', g' \rangle \mid g'}{\langle \overline{\text{at}}(q) s, g \rangle \xrightarrow{\lambda}_p \langle \overline{\text{at}}(q) s', g' \rangle \mid g'}$$



### (Spawn)

---

$$\langle \text{async } s, g \rangle \longrightarrow_p \langle \overline{\text{async}} s, g \rangle$$

### (Async)

$$\frac{\langle s, g \rangle \xrightarrow{\lambda}_p \langle s', g' \rangle \mid g'}{\begin{array}{l} \lambda = \epsilon \quad \langle \overline{\text{async}} s, g \rangle \xrightarrow{\lambda}_p \langle \overline{\text{async}} s', g' \rangle \mid g' \\ \lambda = v \times, v \otimes \quad \langle \overline{\text{async}} s, g \rangle \xrightarrow{v \times}_p \langle \overline{\text{async}} s', g' \rangle \mid g' \end{array}}$$

### (Finish)

$$\frac{\langle s, g \rangle \xrightarrow{\lambda}_p \langle s', g' \rangle}{\langle \text{finish}_\mu s, g \rangle \longrightarrow_p \langle \text{finish}_{\mu \cup \lambda} s', g' \rangle}$$

### (End Finish)

$$\frac{\langle s, g \rangle \xrightarrow{\lambda}_p g' \quad \lambda' = \mathbf{E} \otimes \text{ if } \lambda \cup \mu \neq \emptyset \text{ else } \epsilon}{\langle \text{finish}_\mu s, g \rangle \xrightarrow{\lambda'}_p g'}$$

## (Exception)

$$\frac{}{\langle \text{throw } v, g \rangle \xrightarrow{v \otimes}_p g}$$

## (Try)

$$\frac{\langle s, g \rangle \xrightarrow{\lambda}_p \langle s', g' \rangle \mid g'}{}$$

$$\begin{array}{l} \lambda = \epsilon, v \times \quad \langle \text{try } s \text{ catch } t, g \rangle \xrightarrow{\lambda}_p \langle \text{try } s' \text{ catch } t, g' \rangle \mid g' \\ \lambda = v \otimes \quad \langle \text{try } s \text{ catch } t, g \rangle \longrightarrow_p \langle \{s' \ t\}, g' \rangle \mid \langle t, g' \rangle \end{array}$$

## (Skip)

$$\frac{}{\langle \text{skip}, g \rangle \longrightarrow_p g}$$

Plus rules for expression evaluation