Springer

Book Announcement (January 2010)

A PROCESS ALGEBRAIC APPROACH TO SOFTWARE ARCHITECTURE DESIGN

by

Alessandro Aldini
Univ. of Urbino – Italy

Marco Bernardo
Univ. of Urbino – Italy

Flavio Corradini
Univ. of Camerino – Italy

ABOUT THE BOOK

Concurrency theory, software architecture, system modeling and verification, and dependability and performance evaluation may seem unrelated disciplines, but in reality they are deeply intertwined. Each of them should be part of an integrated view in order to manage successfully the increasing complexity of nowadays software systems.

The book introduces a process algebraic approach to software architecture design. Process algebra, originally conceived for reasoning about the semantics of concurrent programs, provides a foundational basis for the modeling and verification of functional and nonfunctional aspects of communicating concurrent systems. This can be exploited at the software architecture level of design in order to improve the formality of design documents and to enable the analysis of system properties in the early design stages.

The book, which is intended for graduate students and software professionals, does not focus only on theoretical aspects, but also addresses methodological issues and exhibits application examples.

The first part of the book reports on concepts and results of process algebra theory in a quick and comparative way. It contains background material on the syntax and semantics for process calculi as well as on the bisimulation, testing, and trace approaches to the definition of behavioral equivalences for nondeterministic, deterministically timed, and stochastically timed processes.

The second part of the book provides a number of guidelines for a principled transformation of process algebra into an architectural description language. Then, it addresses the detection of architecture-level mismatches by means of a topological reduction process based on behavioral equivalences, as well as the performance-driven selection among alternative designs by associating queueing network models with process algebraic architectural descriptions. Finally, it shows how to trade dependability features and performance indices in the architectural design phase, by resorting to equivalence-checking-based noninterference analysis and standard numerical techniques.

KEY FEATURES OF THE BOOK

- Emphasizes the benefits of using process algebra in the architectural design phase in terms of formality and analyzability of system descriptions.
- Illustrates a friendly component-oriented way of modeling systems that increases the degree of usability of process algebra.
- Covers the component-oriented analysis of both functional and nonfunctional properties of system models by means of process algebraic techniques.
- Explores methodologies dealing with functional verification, performance evaluation, and the architecture-level integration of dependability and performance.
- Provides background material on process calculi for nondeterministic processes, deterministically timed processes, and stochastically timed processes.
- Compares the bisimulation, testing, and trace approaches to the definition of behavioral equivalences for the various kinds of process.

REFERENCES

ISBN: 978-1-84800-222-7 e-ISBN: 978-1-84800-223-4 DOI: 10.1007/978-1-84800-223-4

TABLE OF CONTENTS